

07/436, 506

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	183833 85	@rlad<=19991109 or @ad<=19991109	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 12:55
2	BRS	L2	43809	1 and (two same (storage or storages) same device\$1)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 12:58
3	BRS	L3	732	2 and flag\$1 and (access\$3 same fail\$4)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:00
4	BRS	L4	692	3 and (data same set\$1)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:01
5	BRS	L5	308	4 and (select\$3 same storage\$1 same device\$1)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:04
6	BRS	L6	13	5 and (previous same access\$3 same fail\$3)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:36
7	BRS	L8	2	7 and (virtual same tape same controller\$1)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:13
8	BRS	L7	40	5 and DASD\$1	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:16
9	BRS	L10	3	9 and dasd\$1	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 13:50

	Type	L #	Hits	Search Text	DBs	Time Stamp
10	BRS	L11	0	6282610.uref,bi.	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 14:32
11	BRS	L9	50	5 and (flag\$\$ same fail\$4) and (storage\$1 same device\$1 same fail\$4)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/12/31 14:34

	Document ID	Issue Date	Title	Current OR	Current XRef	Inventor
1	US 20020194294 A1	20021219	Virtual ports for partitioning of data storage	709/213	709/250	Blumenau, Steven M. et al.
2	US 20020129246 A1	20020912	Electronic device for secure authentication of objects such as computers in a data network	713/168		Blumenau, Steven M. et al.
3	US 20020112171 A1	20020815	Systems and methods for secure transaction management and electronic rights protection	713/185	705/51; 713/200	Ginter, Karl L. et al.
4	US 20020048369 A1	20020425	Systems and methods for secure transaction management and electronic rights protection	380/277	380/246; 713/151; 713/194	Ginter, Karl L. et al.
5	US 20020038289 A1	20020328	Method and system for remote delivery of retail banking services	705/42	705/39	Lawlor, Matthew P. et al.
6	US 20020026603 A1	20020228	Method and apparatus for maintaining data coherency	714/6		LeCrone, Douglas E. et al.
7	US 20020007445 A1	20020117	Configuring vectors of logical storage units for data storage partitioning and sharing	711/153	711/217	Blumenau, Steven M. et al.
8	US 20020004884 A1	20020110	DATA STORAGE DEVICE AND CONTROL METHOD THEREFOR	711/112	711/163; 711/203	YAMAKAWA, TERUJI et al.
9	US 6502162 B2	20021231	Configuring vectors of logical storage units for data storage partitioning and sharing	711/5	709/100; 711/153; 711/170	Blumenau, Steven M. et al.
10	US 6493825 B1	20021210	Authentication of a host processor requesting service in a data processing network	713/168	380/278; 713/155	Blumenau, Steven M. et al.
11	US 6493796 B1	20021210	Method and apparatus for maintaining consistency of data stored in a group of mirroring devices	711/114	709/213; 710/22	Arnon, Dan et al.
12	US 6467054 B1	20021015	Self test for storage device	714/42	369/53.17	Lenny, Thomas R.
13	US 6427140 B1	20020730	Systems and methods for secure transaction management and electronic rights protection	705/80	705/53; 713/193	Ginter, Karl L. et al.
14	US 6421711 B1	20020716	Virtual ports for data transferring of a data storage system	709/213	710/36; 711/153	Blumenau, Steven M. et al.
15	US 6389402 B1	20020514	Systems and methods for secure transaction management and electronic rights protection	705/51	380/201; 705/1; 705/37; 705/53; 705/57; 705/80	Ginter, Karl L. et al.
16	US 6363488 B1	20020326	Systems and methods for secure transaction management and electronic rights protection	713/201	705/14; 705/53	Ginter, Karl L. et al.
17	US 6308284 B1	20011023	Method and apparatus for maintaining data coherency	714/6	714/3	LeCrone, Douglas E. et al.
18	US 6295575 B1	20010925	Configuring vectors of logical storage units for data storage partitioning and sharing	711/5	709/100; 711/153; 711/170; 711/171; 711/172; 711/173	Blumenau, Steven M. et al.
19	US 6260120 B1	20010710	Storage mapping and partitioning among multiple host processors in the presence of login state changes and host controller replacement	711/152	707/101; 707/3; 707/4; 707/5; 711/147; 711/149; 711/153; 711/170	Blumenau, Steven M. et al.
20	US 6253193 B1	20010626	Systems and methods for the secure transaction management and electronic rights protection	705/57	705/52	Ginter, Karl L. et al.

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21	US 6237786 B1	20010529	Systems and methods for secure transaction management and electronic rights protection	213/153	380/203; 705/51; 705/58	Ginter, Karl L. et al.
22	US 6202054 B1	20010313	Method and system for remote delivery of retail banking services	705/42	705/26; 705/35; 705/39	Lawlor, Matthew P. et al.
23	US 5982891 A	19991109	Systems and methods for secure transaction management and electronic rights protection	705/54	705/26; 713/167	Ginter, Karl L. et al.
24	US 5949876 A	19990907	Systems and methods for secure transaction management and electronic rights protection	705/80	705/1; 705/39; 705/54	Ginter, Karl L. et al.
25	US 5917912 A	19990629	System and methods for secure transaction management and electronic rights protection	713/187	705/40; 709/312; 713/164	Ginter, Karl L. et al.
26	US 5915019 A	19990622	Systems and methods for secure transaction management and electronic rights protection	705/54	705/26; 705/400; 713/200	Ginter, Karl L. et al.
27	US 5910987 A	19990608	Systems and methods for secure transaction management and electronic rights protection	705/52	705/30	Ginter, Karl L. et al.
28	US 5892900 A	19990406	Systems and methods for secure transaction management and electronic rights protection	713/200	713/201	Ginter, Karl L. et al.
29	US 5870724 A	19990209	Targeting advertising in a home retail banking delivery service	705/42	235/379; 235/380; 705/14; 705/43	Lawlor, Matthew P. et al.
30	US 5818792 A	19981006	Semiconductor memory device and memory module using the same	365/230.0 8	365/200	Sasaki, Toshio et al.
31	US 5809543 A	19980915	Fault tolerant extended processing complex for redundant nonvolatile file caching	711/162	711/120; 711/167; 714/14; 714/6	Byers, Larry L. et al.
32	US 5802365 A	19980901	Dynamic device matching using driver candidate lists	709/321	710/10; 710/104; 713/1	Kathail, Pradeep et al.
33	US 5691952 A	19971125	Semiconductor memory device and memory module using the same	365/230.0 8	365/200	Sasaki, Toshio et al.
34	US 5630076 A	19970513	Dynamic device matching using driver candidate lists	710/104	709/321; 710/10; 713/1	Saulpaugh, Thomas E. et al.
35	US 5621887 A	19970415	Fault tolerant disk management system which tests failed disks during varied time intervals which are based upon the criticality associated with the failed disks	714/42	714/23; 714/55	Chatterji, Sanjoy
36	US 5574856 A	19961112	Fault indication in a storage device array	714/52	714/6	Morgan, Lisa A. et al.
37	US 5555373 A	19960910	Inactivity monitor for trusted personal computer system	713/202		Dayan, Richard A. et al.
38	US 5553285 A	19960903	File system for a plurality of storage classes	707/202	711/100; 714/44; 714/767	Krakauer, Arno S. et al.
39	US 5553231 A	19960903	Fault tolerant memory system	714/5	714/797	Papenberg, Robert L. et al.
40	US 5542064 A	19960730	Data read/write method by suitably selecting storage units in which multiple copies of identical data are stored and apparatus therefor	711/114	711/162; 711/168; 711/221	Tanaka, Atsushi et al.
41	US 5386551 A	19950131	Deferred resource recovery	714/46	709/104	Chikira, JoEtta S. et al.
42	US 5379415 A	19950103	Fault tolerant memory system	714/5	714/814	Papenberg, Robert L. et al.
43	US 5379411 A	19950103	Fault indication in a storage device array	714/6		Morgan, Lisa A. et al.

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44	US 5345575 A	19940906	Write optimized disk storage device	711/112	360/78.04	English, Robert M. et al.
45	US 5220501 A	19930615	Method and system for remote delivery of retail banking services	705/40	379/93.18; 380/29; 705/42; 705/43; 705/70; 705/77; 902/24	Lawlor, Matthew P. et al.
46	US 5191581 A	19930302	Method and apparatus for providing high performance interconnection between interface circuits coupled to information buses	370/364	326/78; 326/90; 710/315	Woodbury, Mark P. et al.
47	US 4993030 A	19910212	File system for a plurality of storage classes	714/6	714/763; 714/777	Krakauer, Arno S. et al.
48	US 4280285 A	19810728	Simulator complex data transmission system having self-testing capabilities	434/30	434/43; 434/58	Haas, Robert N.
49	US 4207687 A	19800617	Simulator complex data transmission method and system	434/49		Haas, Robert N. et al.
50	US 3955180 A	19760504	Table driven emulation system	703/26		Hirtle, Allen C.